

Fig.1A

VGA

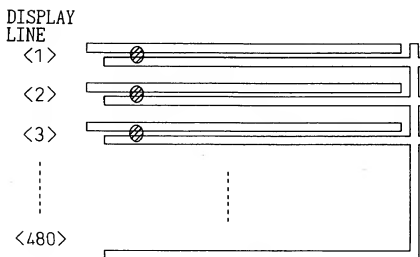


Fig.1B

ALIS(1024)

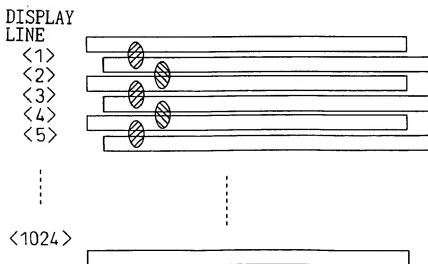




Fig.3A

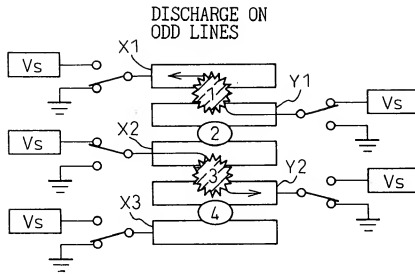


Fig.3B

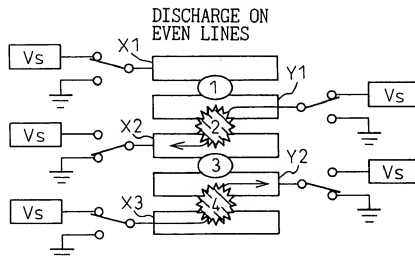
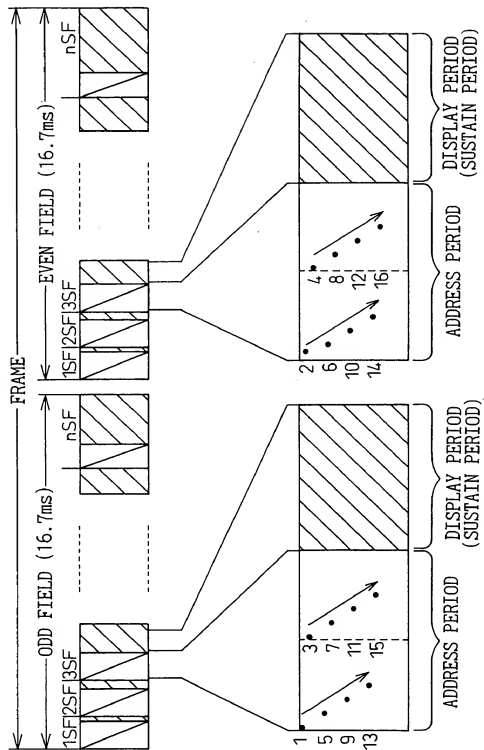


Fig.4



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Fig. 5

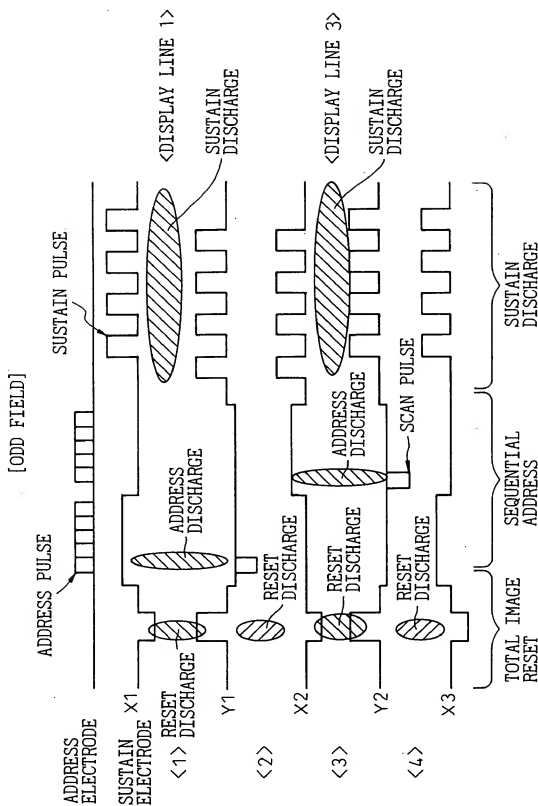


FIG. 6

Fig. 6

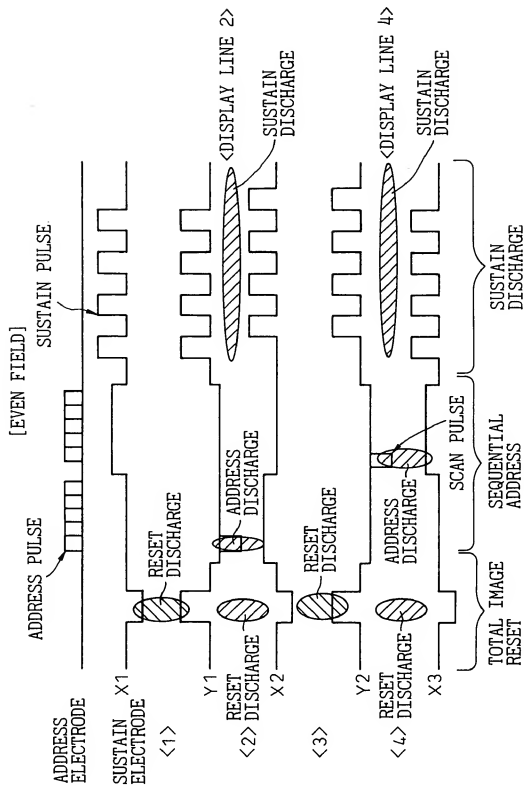


Fig.7

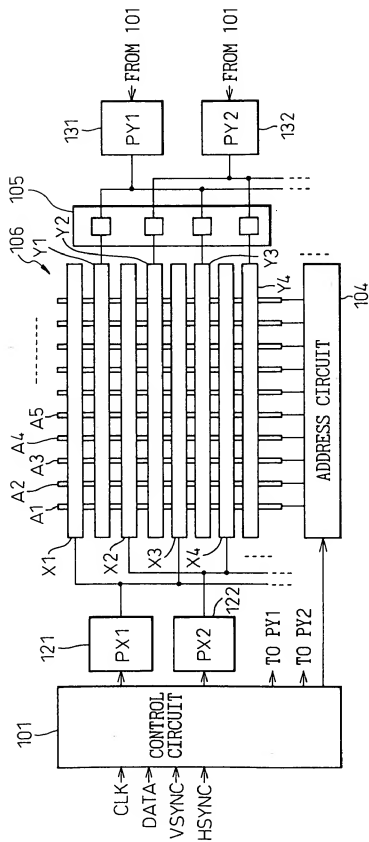


Fig. 8

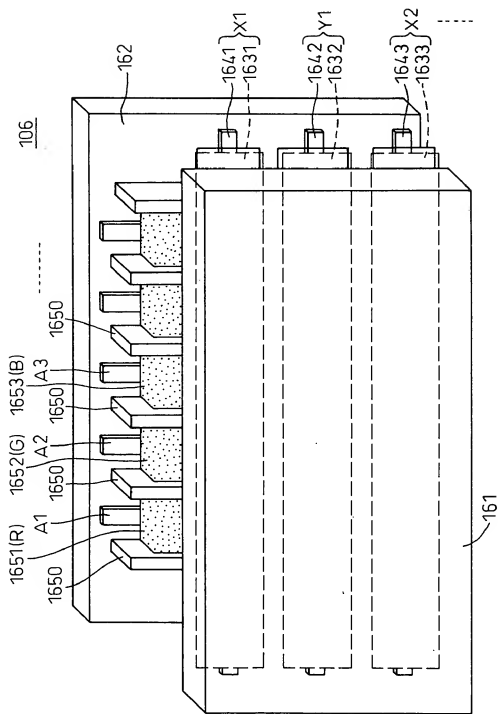
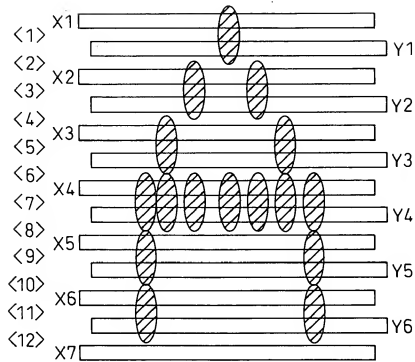




Fig.9



0000007.120301

Fig.10

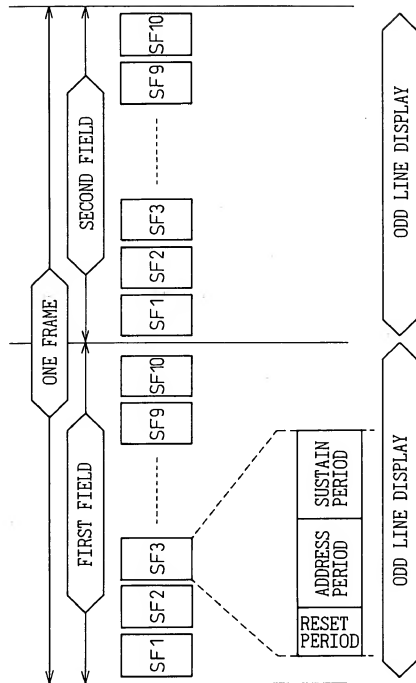
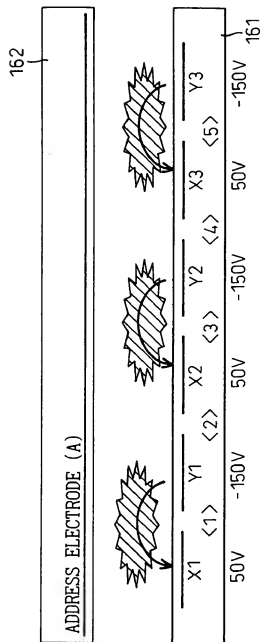


Fig.11



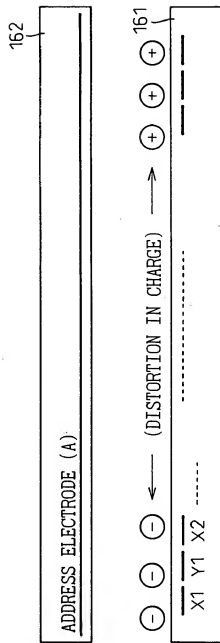


Fig.12A

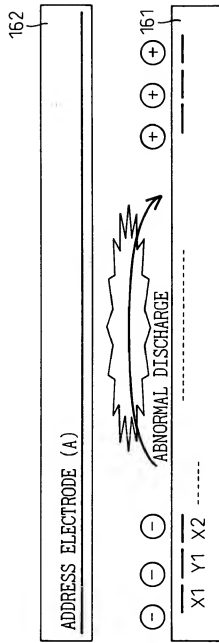


Fig.12B

FIG. 13

Fig.13

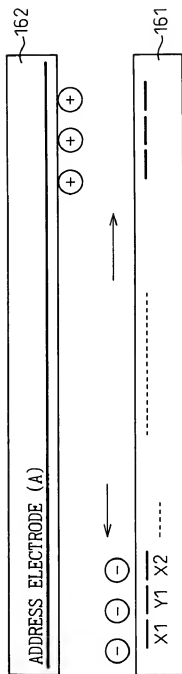


Fig.14

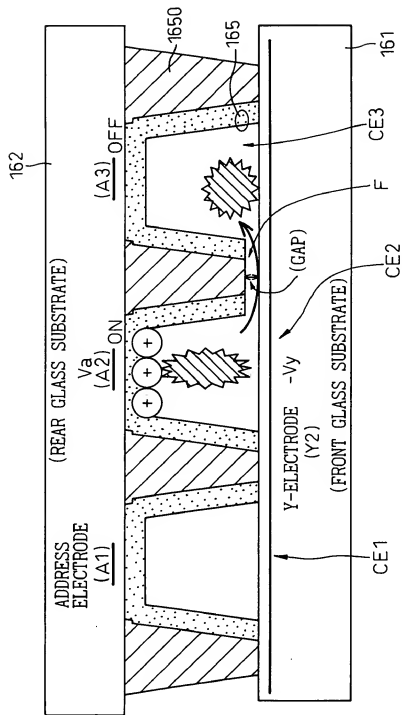


Fig.15A

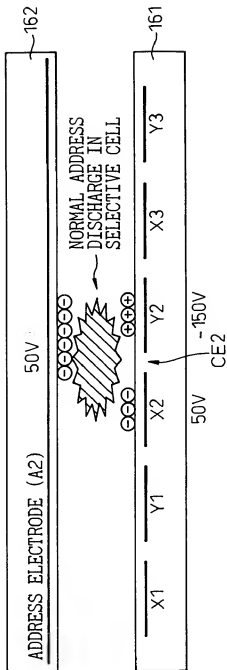


Fig.15B

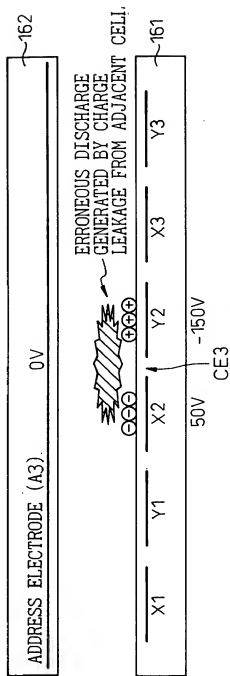


Fig.16

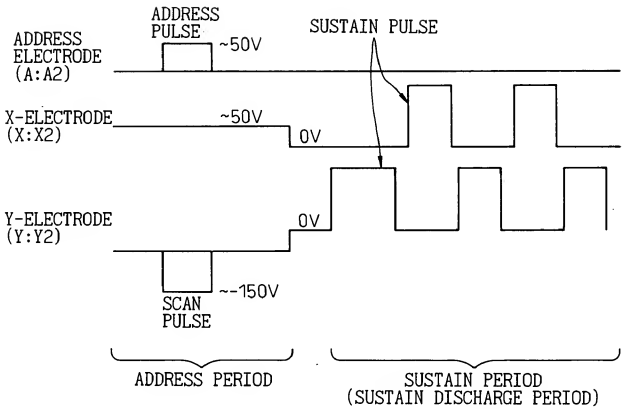




Fig.17

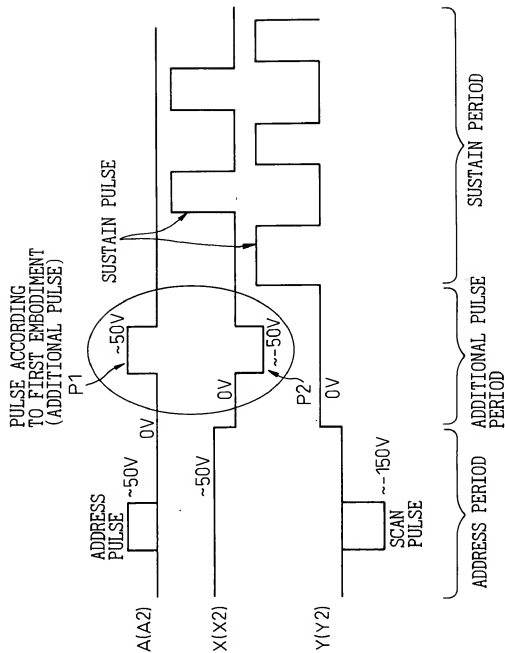


Fig.18A

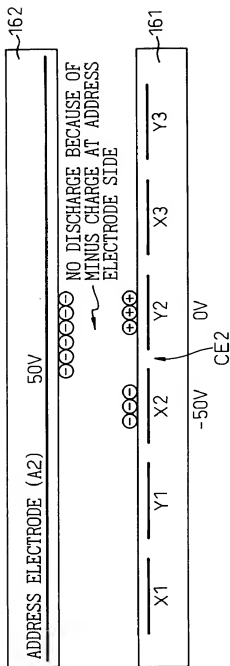


Fig.18B

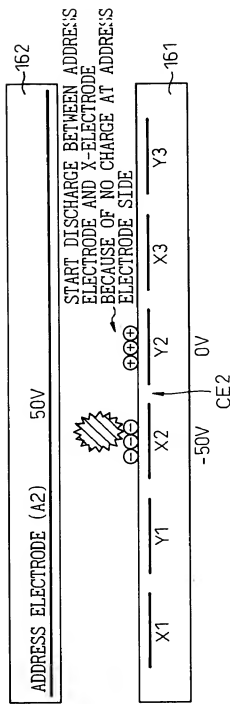


Fig.19

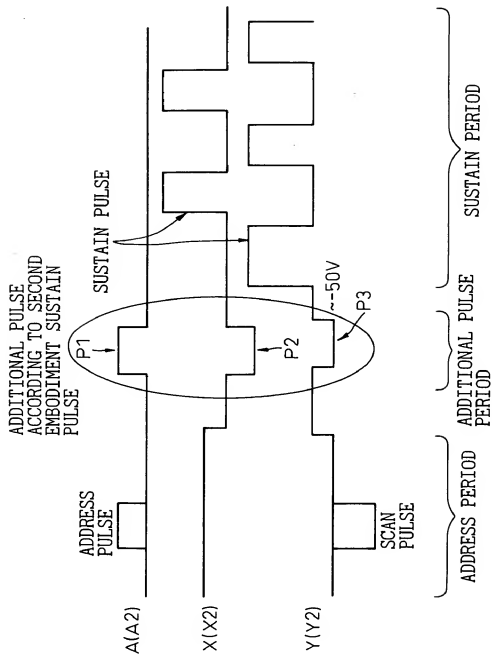


Fig.20

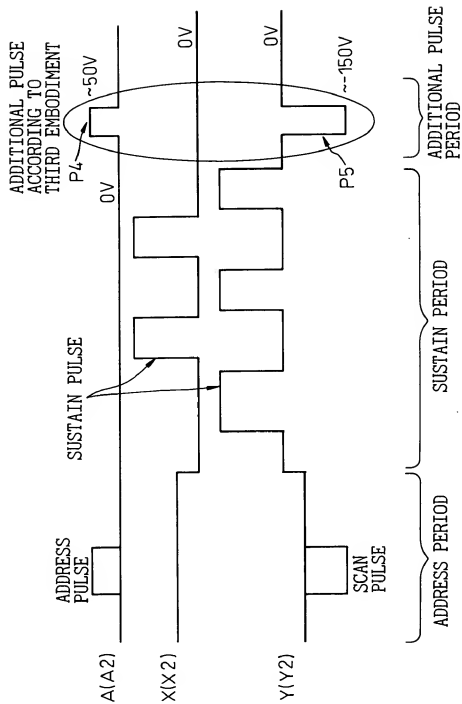


Fig. 21

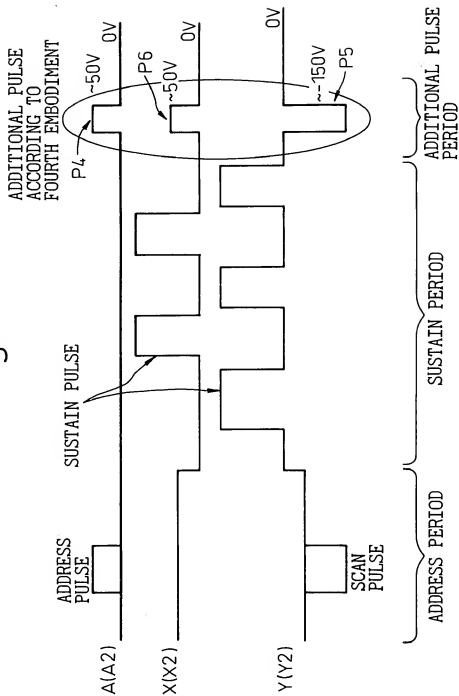


FIG. 22

Fig.22

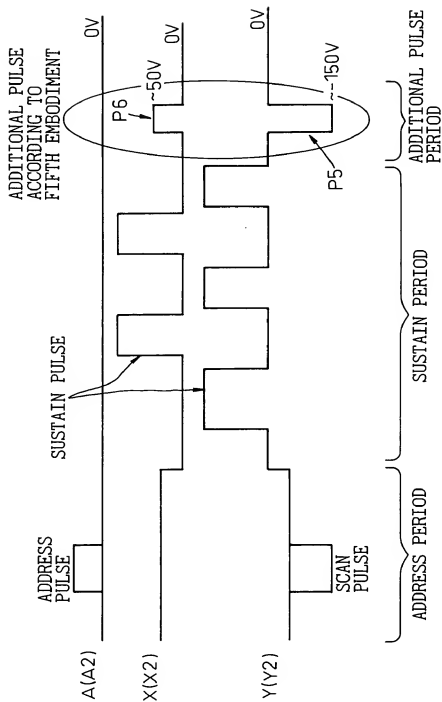


FIG. 23

Fig. 23

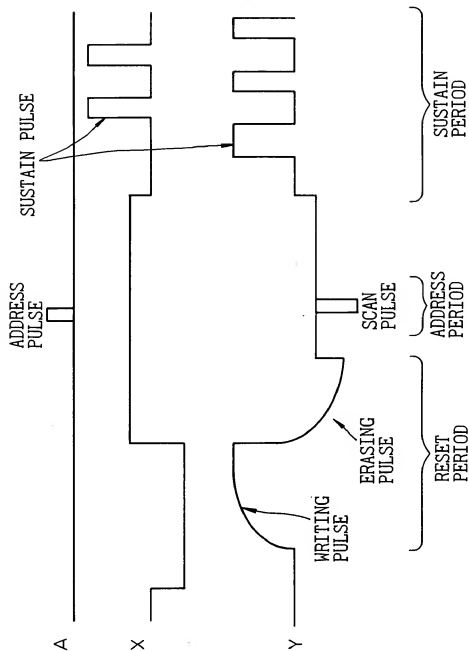


Fig.24

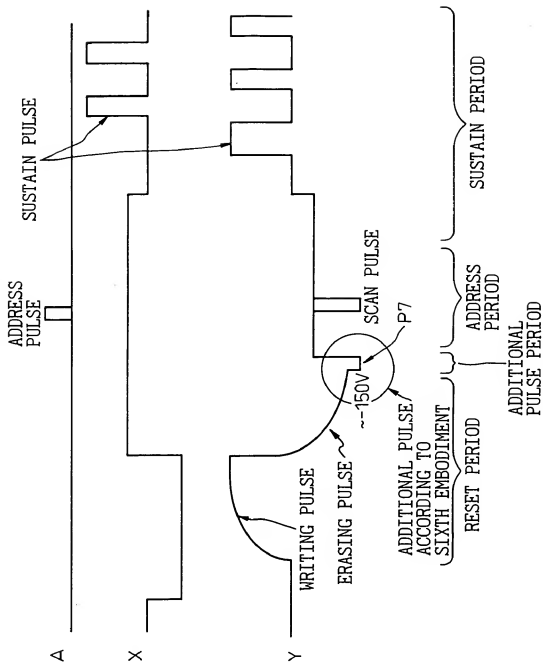
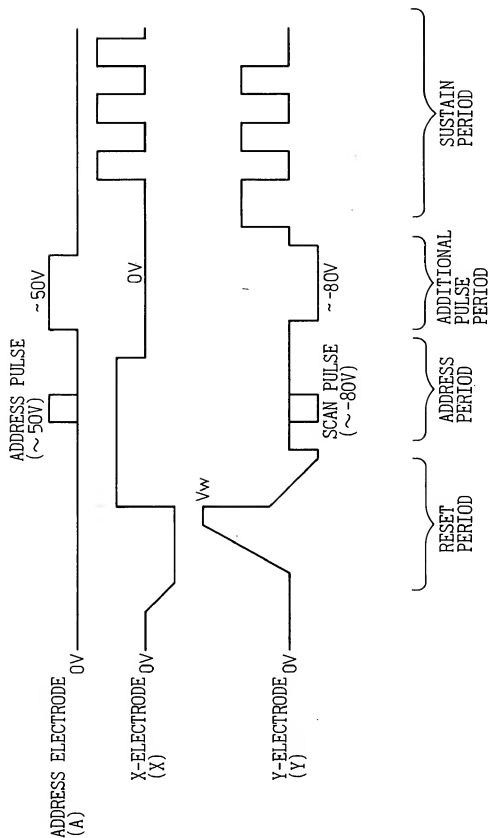




Fig.25



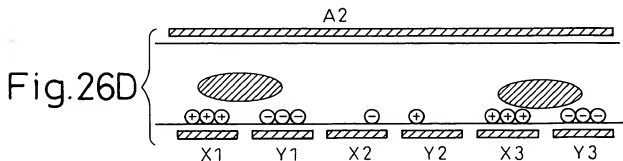
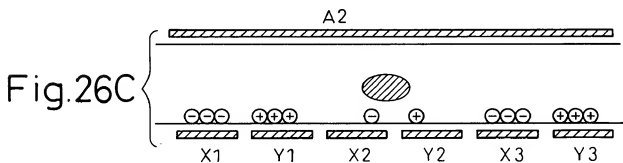
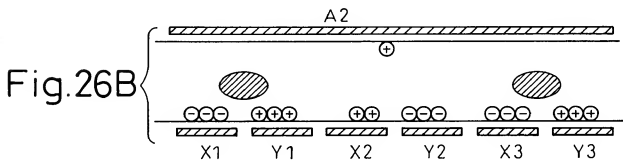
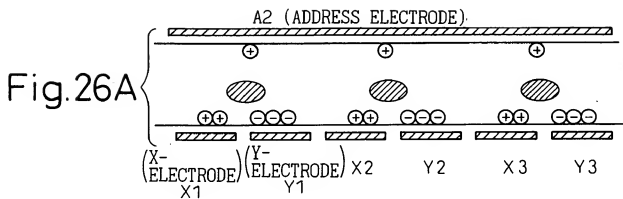


Fig.27

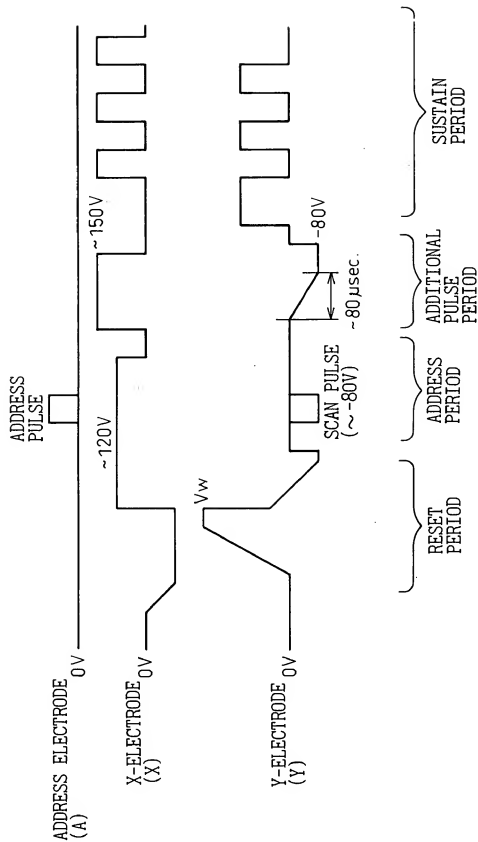


Fig.28

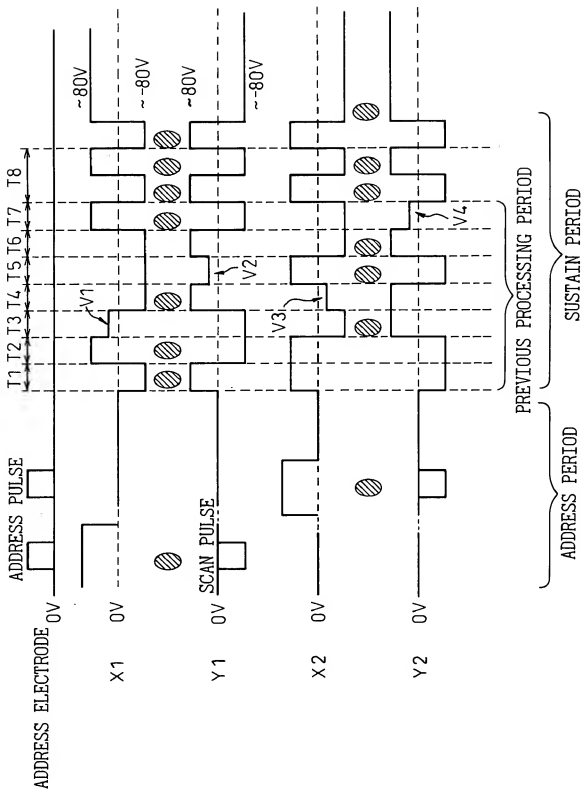
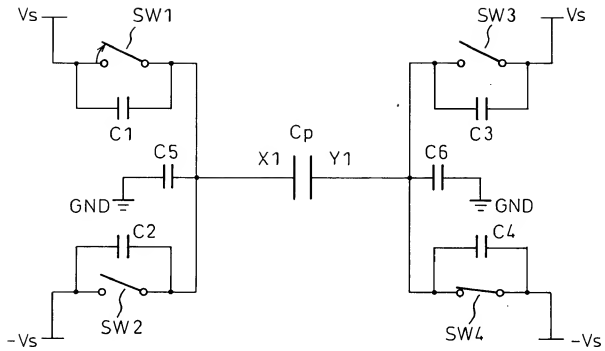


Fig.29



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Fig. 30

